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APPLICATION NO.	F	FILING DATE FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/960,482	09/960,482 09/24/2001 22428 7590 09/16/2004		Brian M. Foley	033337/0103 5798			
22428				EXAM	INER		
FOLEY A	ND LAR	DNER	LAVARIAS, ARNEL C				
SUITE 500 3000 K STI				ART UNIT	PAPER NUMBER		
	WASHINGTON, DC 20007			2872			
				DATE MAIL ED: 00/16/200	4		

Please find below and/or attached an Office communication concerning this application or proceeding.

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Application No.	Applicant(s)		
09/960,482	FOLEY, BRIAN M.		
Examiner	Art Unit	_	
Arnel C. Lavarias	2872		

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 01 September 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

U.S. Patent and Trademark Office PTOL-303 (Rev. 11-03) **GROUP 2800**

Continuation of 5. does NOT place the application in condition for allowance because: Applicant's remarks and arguments have been noted, but are not found persuasive. Additionally, with regard to Applicant's argument that the statement that '... it is well known in the art to have the patch panel 64 include circuit card or be replaced by circuit cards' is not supported by the prior art or the knowledge of one of ordinary skill in the art, the Examiner directs the Applicant to U.S. Patent No. 5394503 to Dietz, Jr. et al. Dietz, Jr. et al. is being cited to evidence the fact that it would have been obvious and apparent to one of ordinary skill to have the fiber optic patch panel include circuit cards (See in particular Figures 2-3; 79, 80 in Figure 3).